



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Hiroshi Tayanaka

Docket No.: **P97,0027**

SERIAL NO:

08/818,239

GROUP ART UNIT:

2813

FILING DATE:

March 14, 1997

EXAMINER: K. Christianson

FOR:

"METHOD FOR MAKING THIN FILM SEMICONDUCTOR,

SOLAR CELL, AND LIGHT EMITTING DIODE"

Assistant Commissioner for Patents Washington, D.C. 20231

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RESPONSE

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Dear Sir:

This is a response to the Office Action mailed September 22, 1998 in the aboveidentified application.

In the Office Action, restriction was required as between:

- I. Claims 1-16, drawn to a method of making a thin film semiconductor, classified in class 438, subclass 758.
- II. Claims 17-22, drawn to a method of making a solar cell, classified in class 136, subclass 252.
- III. Claims 23-25, drawn to a method for making a light emitting diode, classified in class 438, subclass 22.

In response to the restriction requirement, Applicant hereby elects in writing Group

I, Claims 1-16 for prosecution on the merits first in this application.



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Early and favorable action with respect to Group I, Claims 1-16 is respectfully requested.

Respectfully submitted,

RV.

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OCT 1 9 1998

CERTIFICATE OF MAILING

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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. C. 20231 on <u>October 9, 1998</u>.

John W. Cornell
Name of Applicant's Attorney

Signature

October 9, 1998

Date